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A recapitulation of the architecture's

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features can be found in chapter 2.2.7 Intel® 64 Architecture in manual Basic Architecture: Intel 64 architecture increases the linear address space for software to 64 bits and supports physical address space up to 40 bits. The technology also introduces a new operating mode referred to as IA-32e mode.



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Intel® 64 and IA-32 Architectures  
Software Developer's Manual Volume 4:  
Model-Specific Registers Submitted: May  
01, 2018 Last updated: May 27, 2020.  
File: 335592-sdm-vol-4.pdf; Size: 2.71

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Intel® 64 and IA-32 Architectures  
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Number: 248966-033 June 2016. Title:

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Intel® 64 and IA-32 Architectures  
Optimization Reference Manual Author:  
Intel Corporation Keywords: 64  
architecture, IA-32, core processors,  
248966 Created Date:

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x86 and amd64 instruction reference.



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Derived from the May 2019 version of the Intel® 64 and IA-32 Architectures Software Developer's Manual. Last updated 2019-05-30. THIS REFERENCE IS NOT PERFECT. It's been mechanically separated into distinct files by a dumb script.

**x86 and amd64 instruction**

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## **reference - felixcloutier.com**

The Intel 64 and IA-32 Architectures Software Developer's Manual consists of three volumes: Basic Architecture , Order Number 253665; Instruction Set Reference A-Z , Order Number 325383; System Programming Guide , Order Number 325384.

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## **Intel® 64 and IA-32 Architectures Software Developer's ...**

INSTRUCTIONS The Intel 64 and IA-32 architectures define several serializing instructions. These instructions force the processor to complete all modifications to flags, registers, and memory by previous instructions and to drain all buffered writes to memory

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before the next instruction is fetched and executed.

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x86 is a family of instruction set architectures initially developed by Intel based on the Intel 8086 microprocessor and its 8088 variant. The 8086 was introduced in 1978 as a fully 16-bit

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extension of Intel's 8-bit 8080 microprocessor, with memory segmentation as a solution for addressing more memory than can be covered by a plain 16-bit address. The term "x86" came into being because the ...

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Double-precision floating-point (64-bits) 8. Double extended-precision floating-point (80-bits) 8. Quadword. 8. Double quadword. 16. Segment Selector. 2. 32-bit Far Pointer. 2. 48-bit Far Pointer. 4. 32-bit Pointer. 4. GDTR, IDTR, LDTR, or Task Register Contents. 4. FSTENV/FLDENV Save Area. 4 or 2, depending on operand size.

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FSAVE/FRSTOR Save Area

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